

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re U.S. Patent Application of
YAMAMOTO et al.

Application Number: 10/817,004

Filed: April 5, 2004

For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE
AND MANUFACTURING METHOD THEREOF

Attorney Docket No. HITA.0536

Commissioner of Patents
P.O. Box 1450
Alexandria, VA 22313-1450



) Art Unit 2826

) Examiner Ahmed N. Sefer

PETITION FOR EXTENSION OF TIME

Sir:

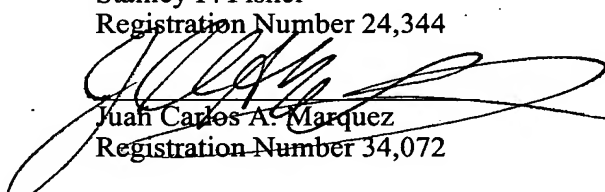
Applicants request that the time for taking action in this case be extended pursuant to 37 C.F.R. § 1.136(a) for **three (3) months**. The fee set in 37 C.F.R. § 1.17 for the extension of time is **\$1,020.00**.

Please charge any additional fee required for this extension of time to Deposit Account 08-1480. A duplicate copy of this paper is enclosed.

Also filed is a *Response to an Office Action*.

Respectfully submitted,

Stanley P. Fisher
Registration Number 24,344


Juan Carlos A. Marquez
Registration Number 34,072

Thomas J. McWilliams
Registration No. 44,930

REED SMITH LLP
3110 Fairview Park Drive
Suite 1400
Falls Church, Virginia 22042
(703) 641-4200
May 16, 2006

05/17/2006 SZEWDIE1 00000013 10817004

01 FC:1253

1020.00 0P